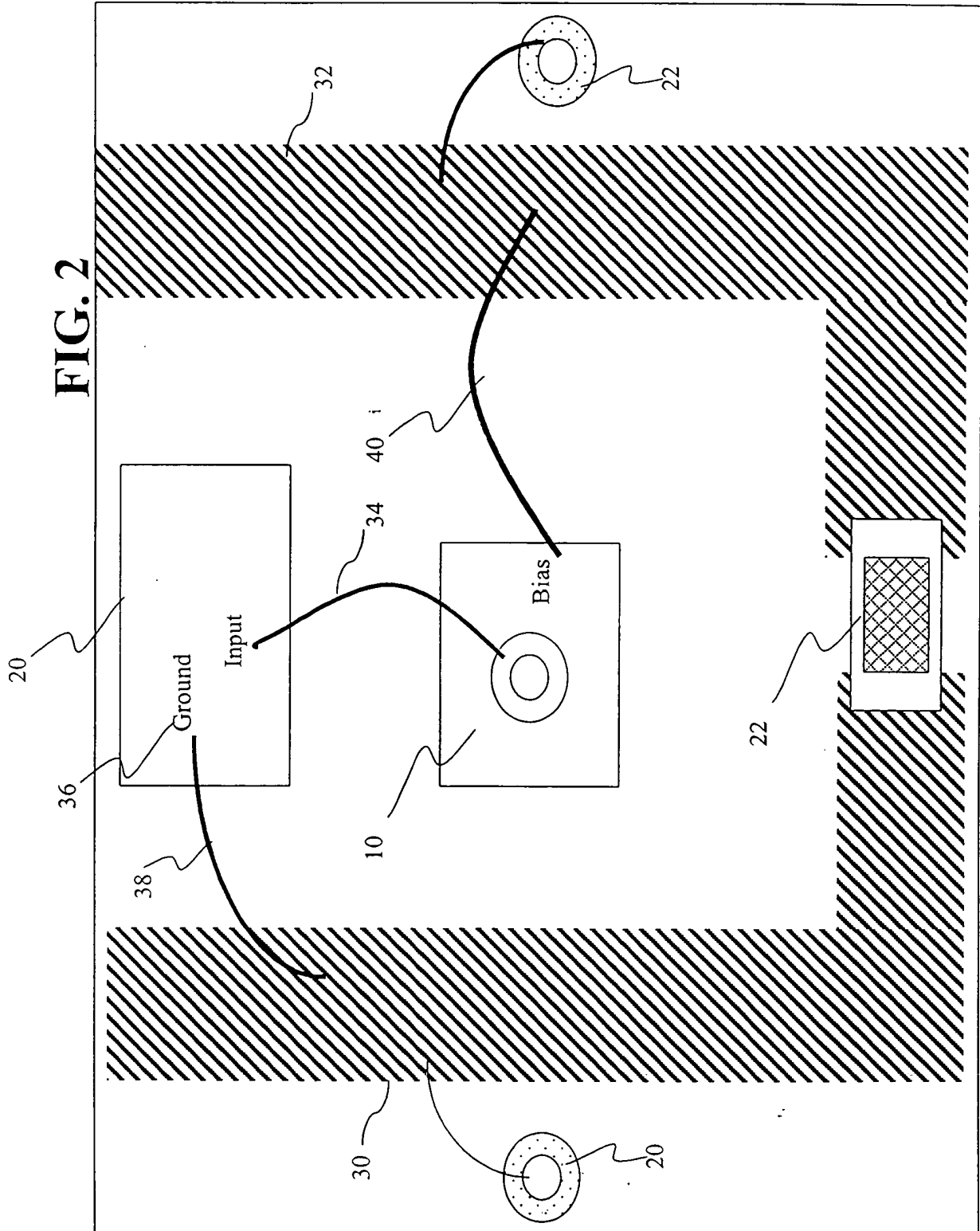


FIG. 1

The diagram shows a circuit for protecting the input of a device 20. A Bias input 12 is connected to a Cathode 16 of a diode 10. The Cathode 16 is also connected to a node 21. The Anode 14 of the diode 10 is connected to the Input of device 20. A capacitor 22 is connected between the Cathode 16 and the Anode 14. The Input of device 20 is also connected to a Ground terminal. The circuit is enclosed in a dashed box 18.

FIG. 2



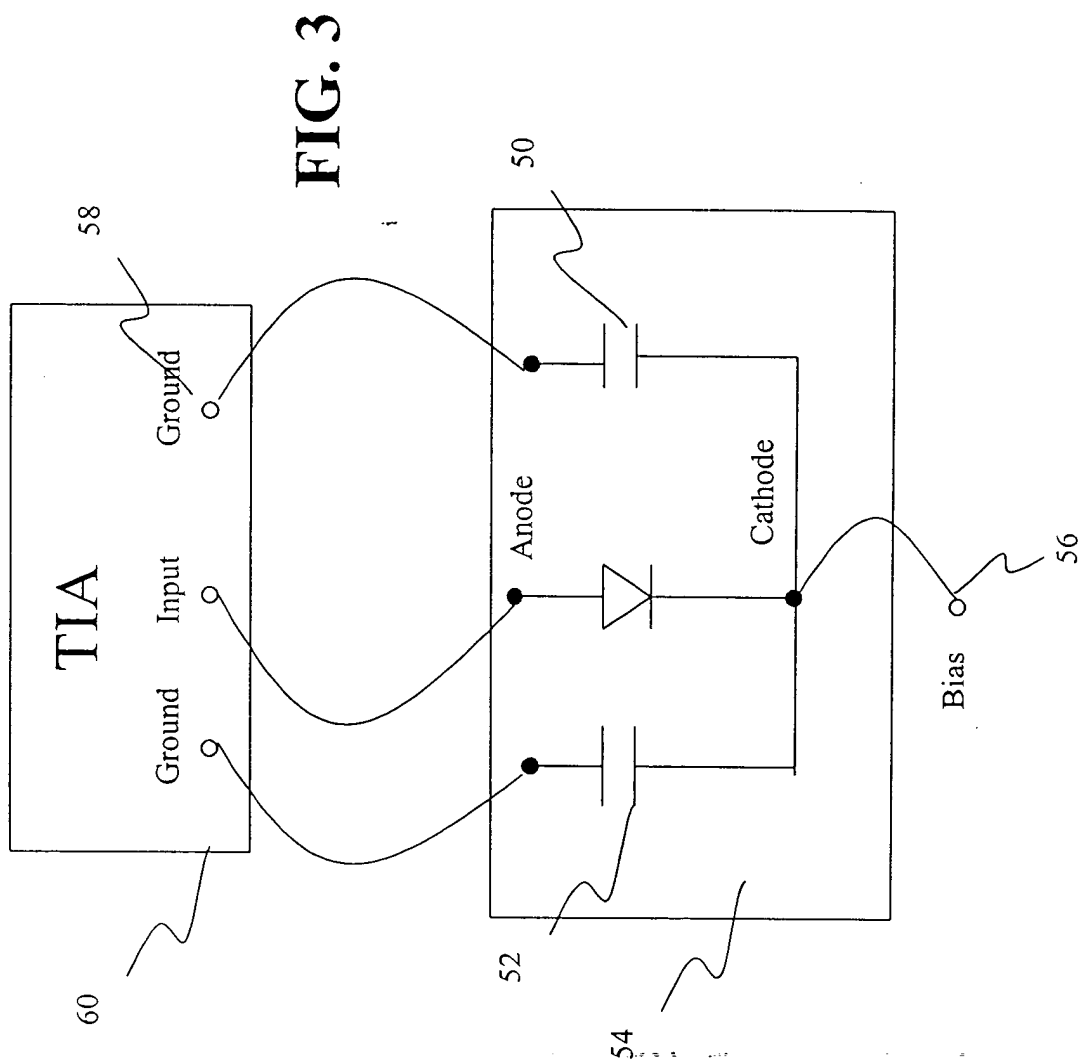
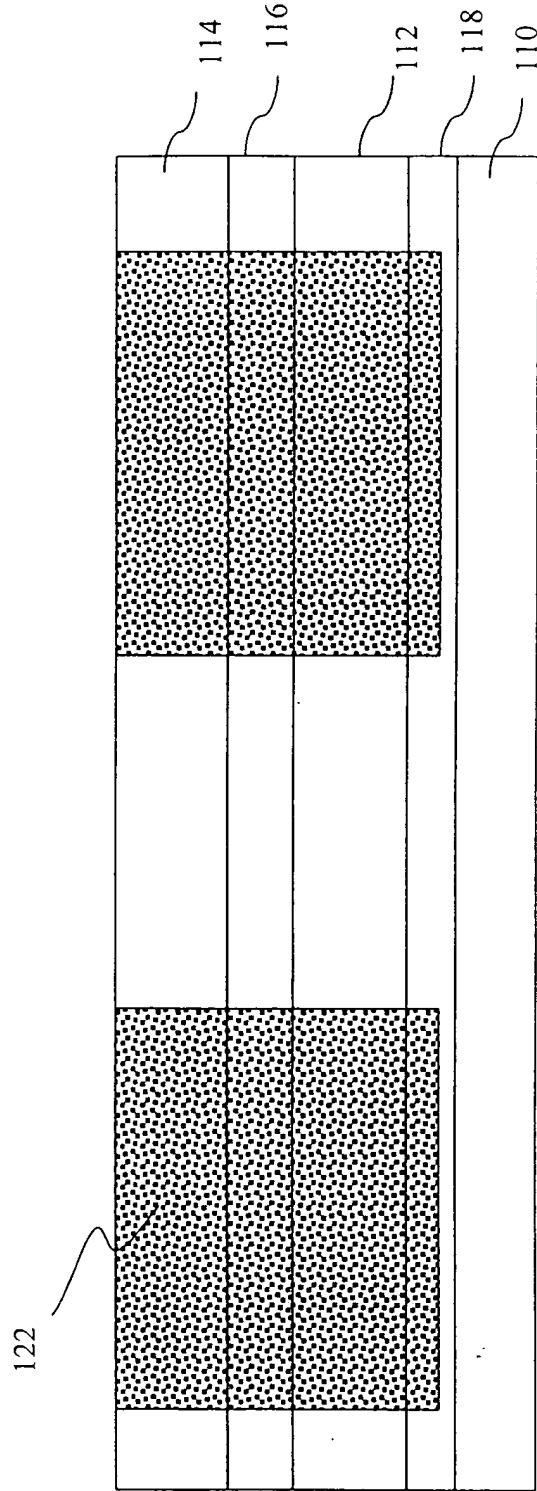


FIG. 4



2021-09-24 10:00:00

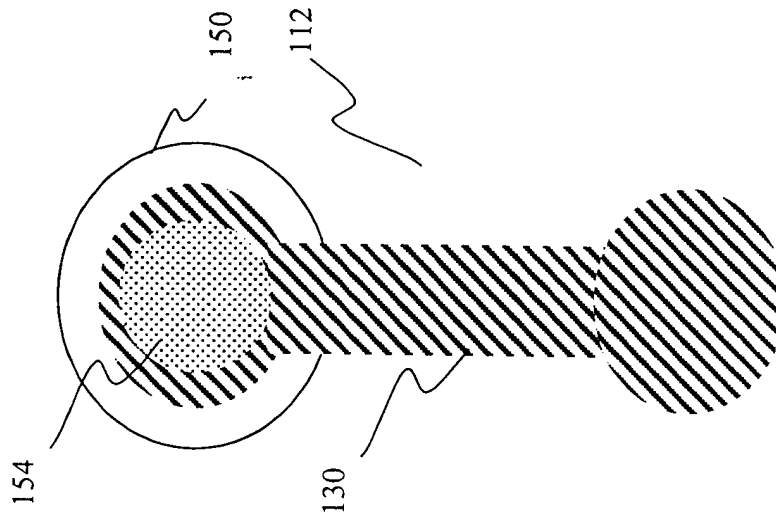
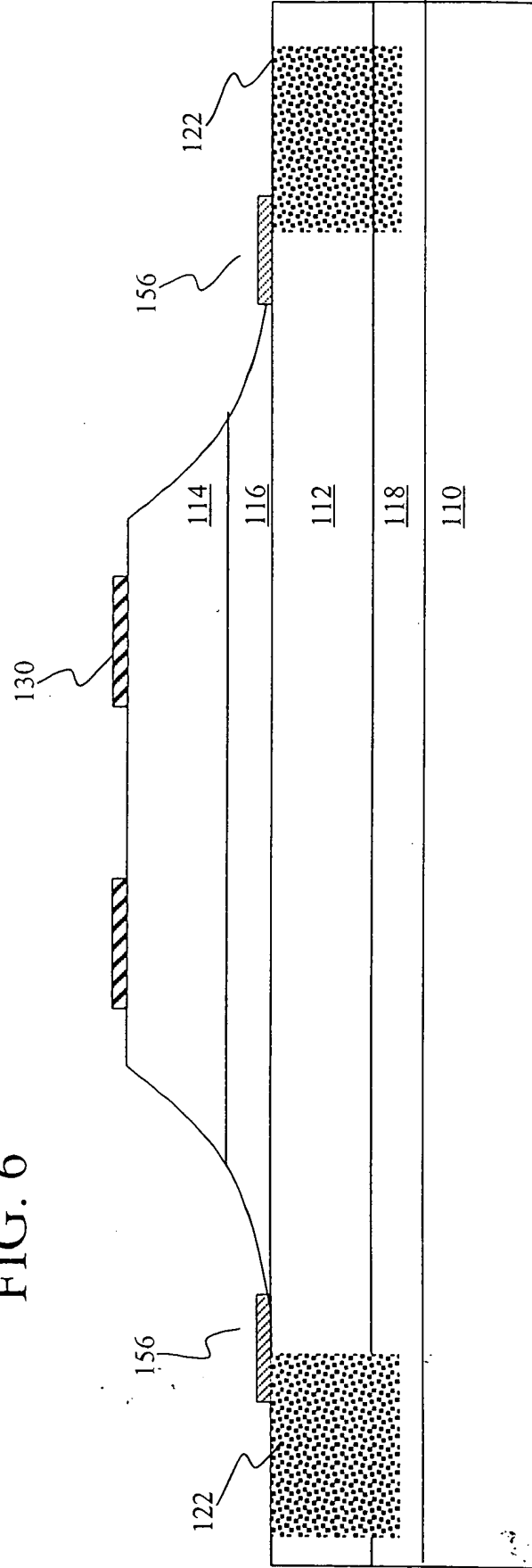


FIG. 5

2025-09-24 10:00:00

FIG. 6



20240" SBLEET

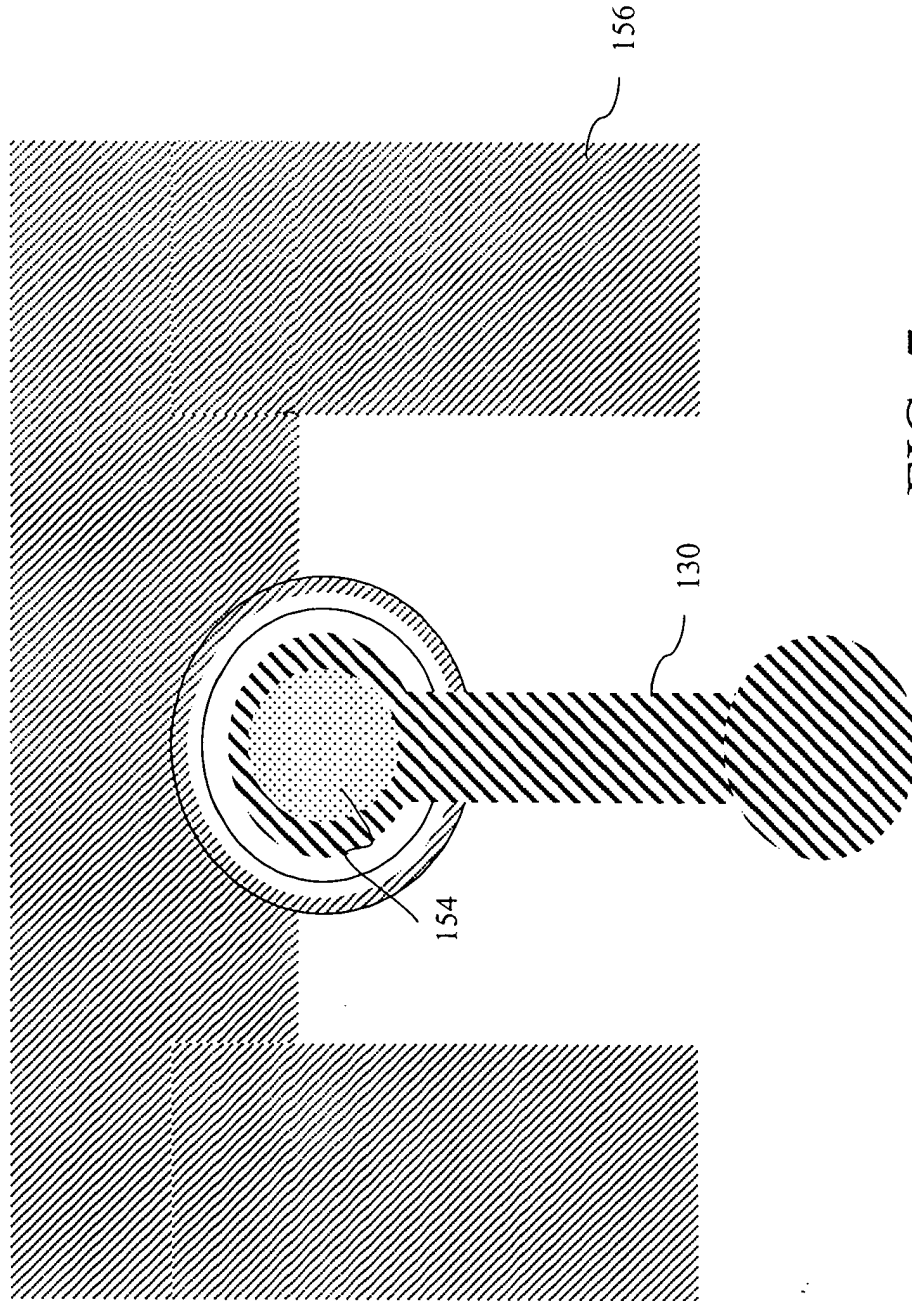



FIG. 7

202720*9844F001

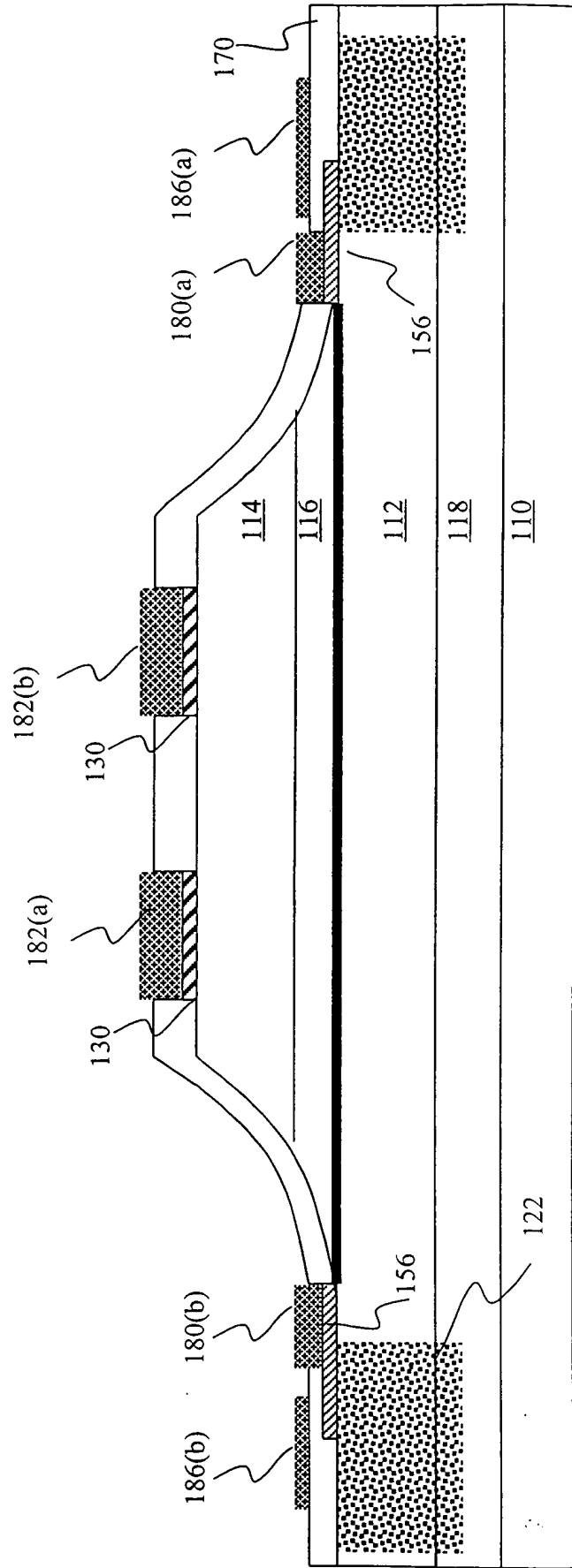
108 

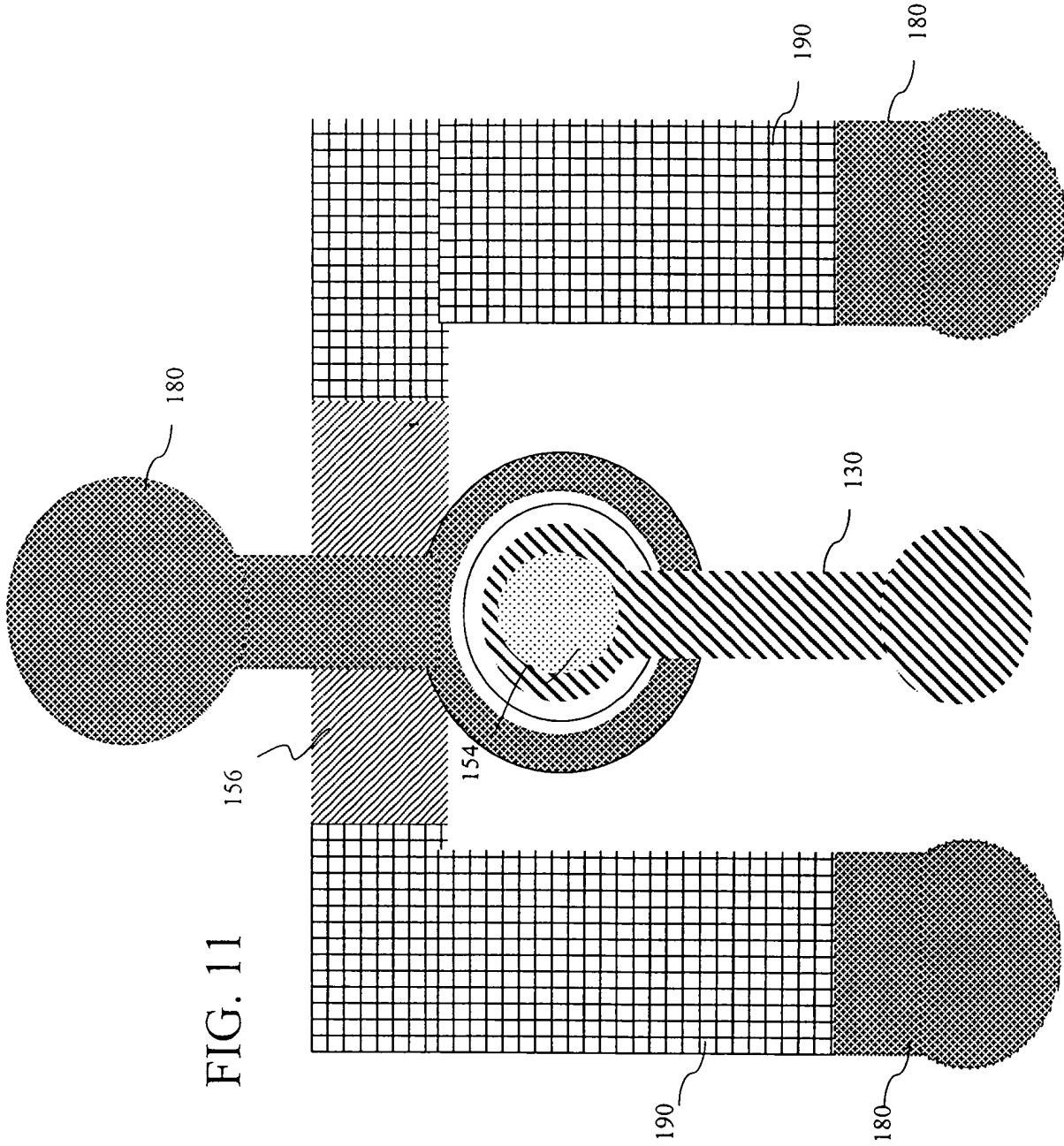


—



i





204720" 9844T00T

FIG. 12

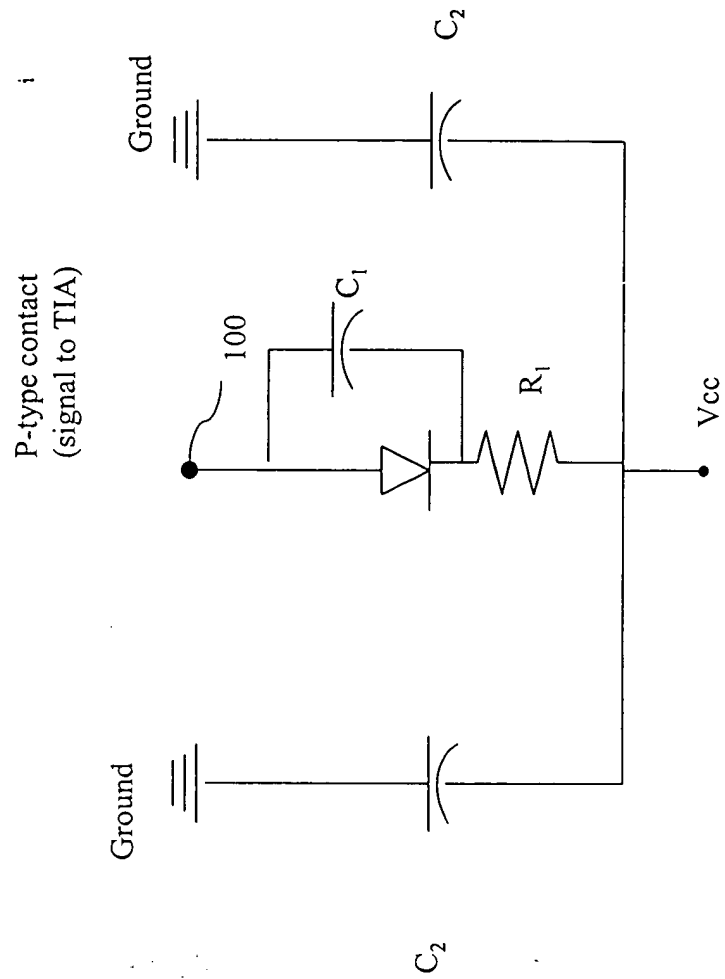


FIG. 13

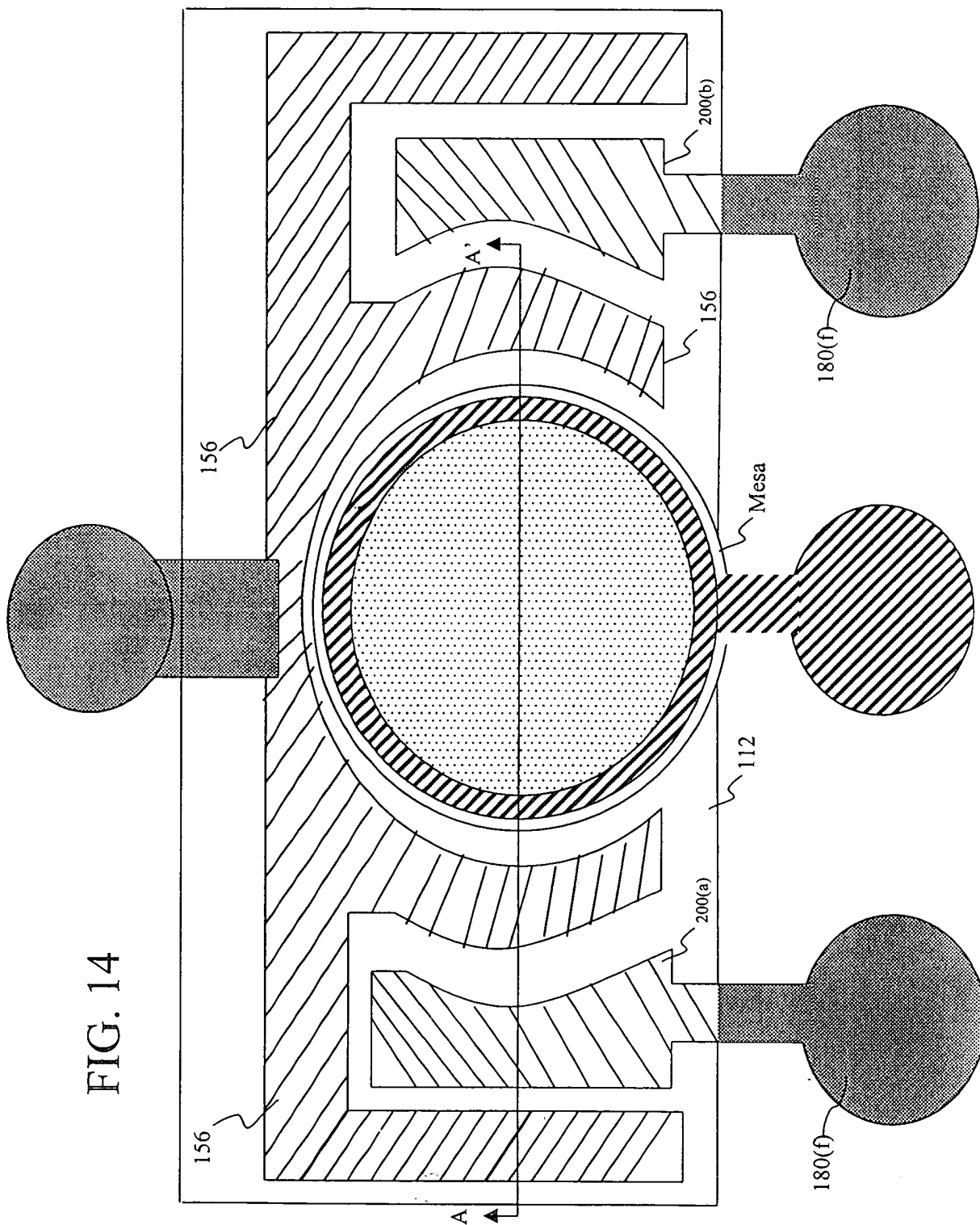
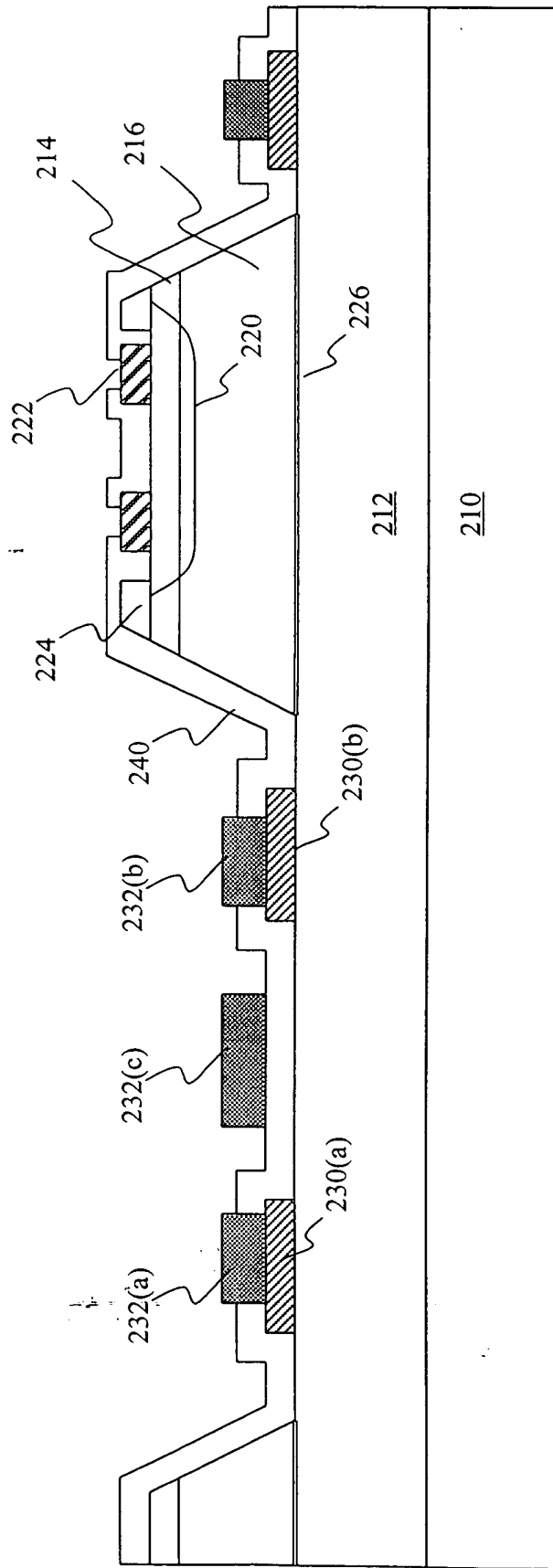
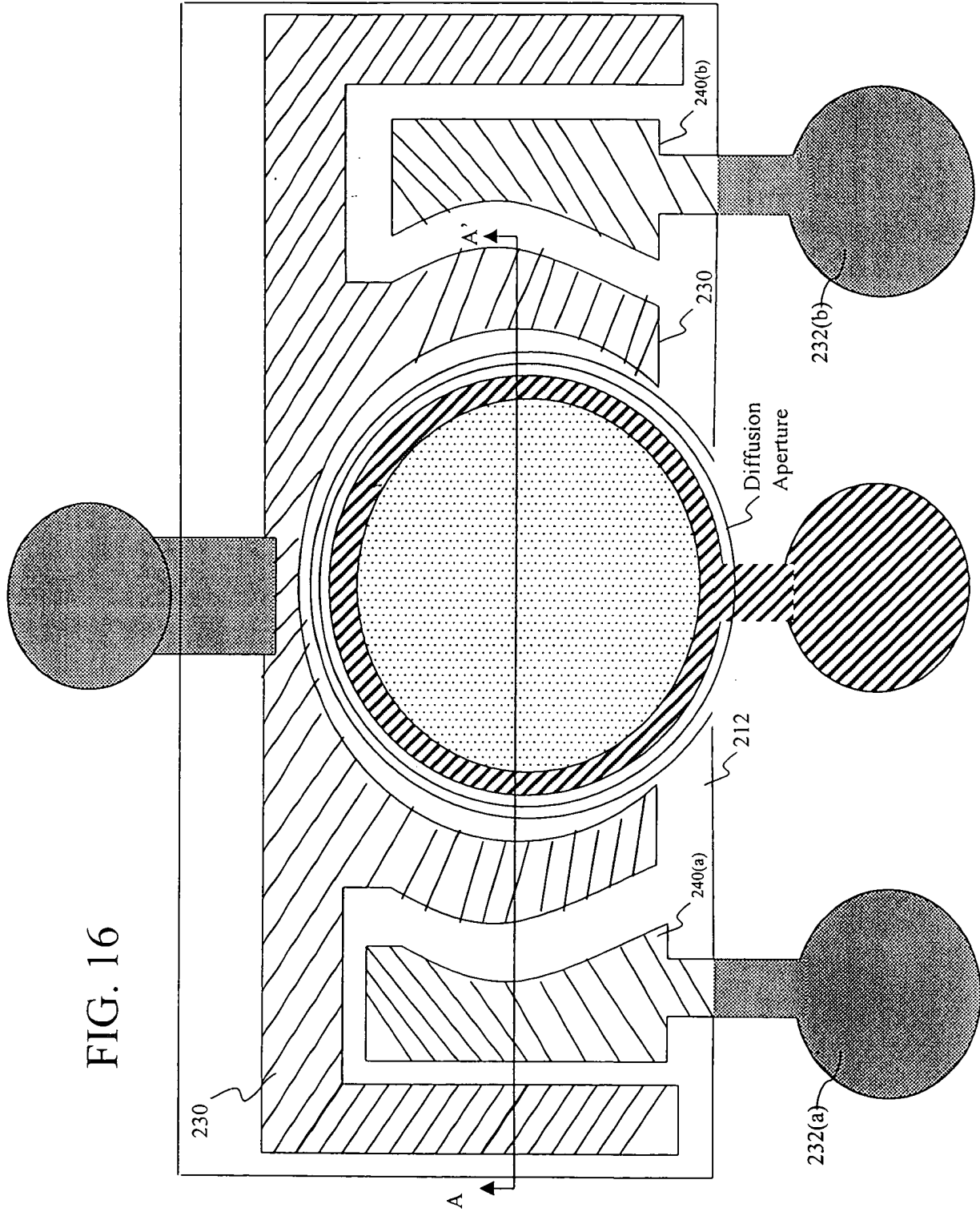


FIG. 14

202209844001

FIG. 15





202720" 98447001

FIG. 17

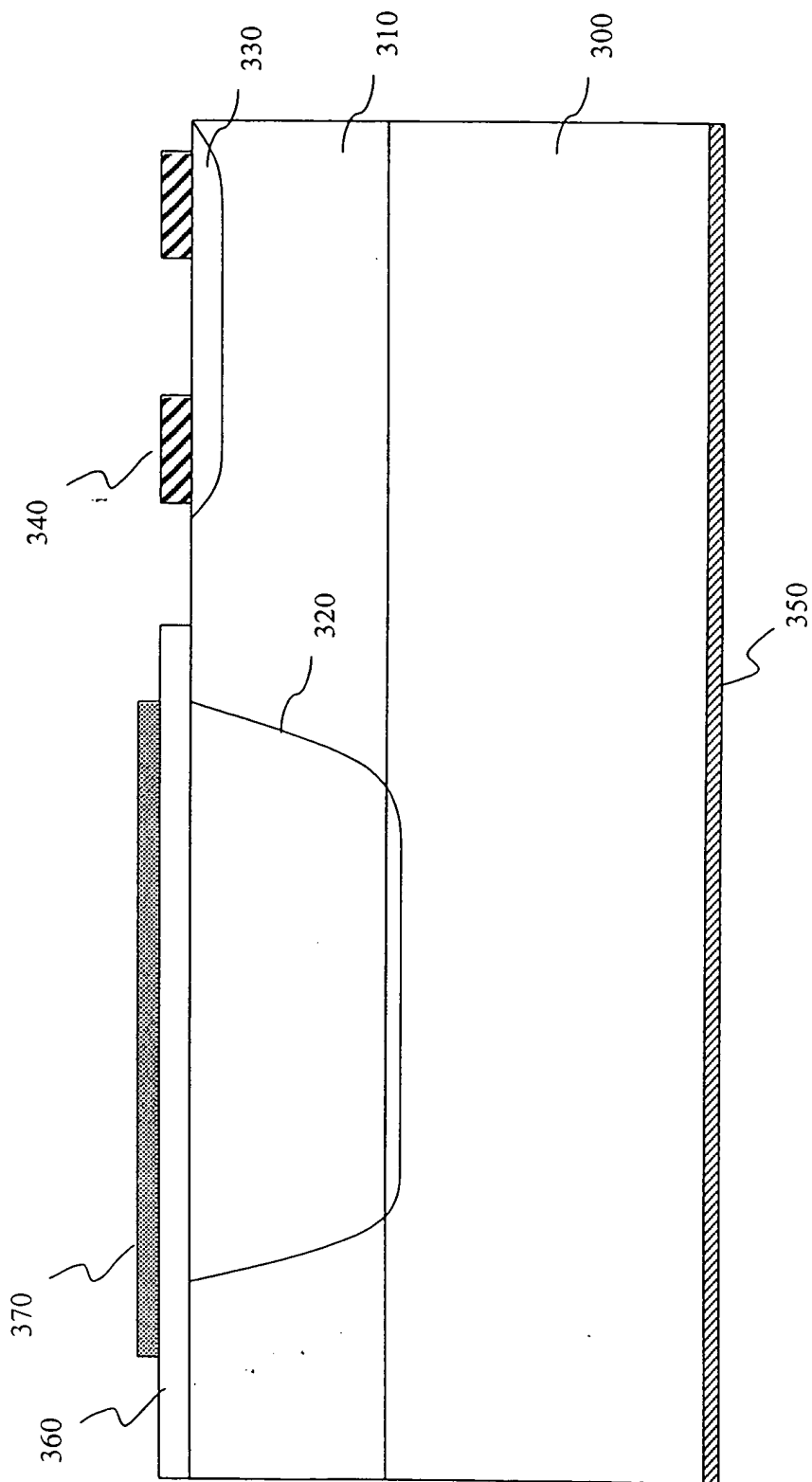


FIG. 18

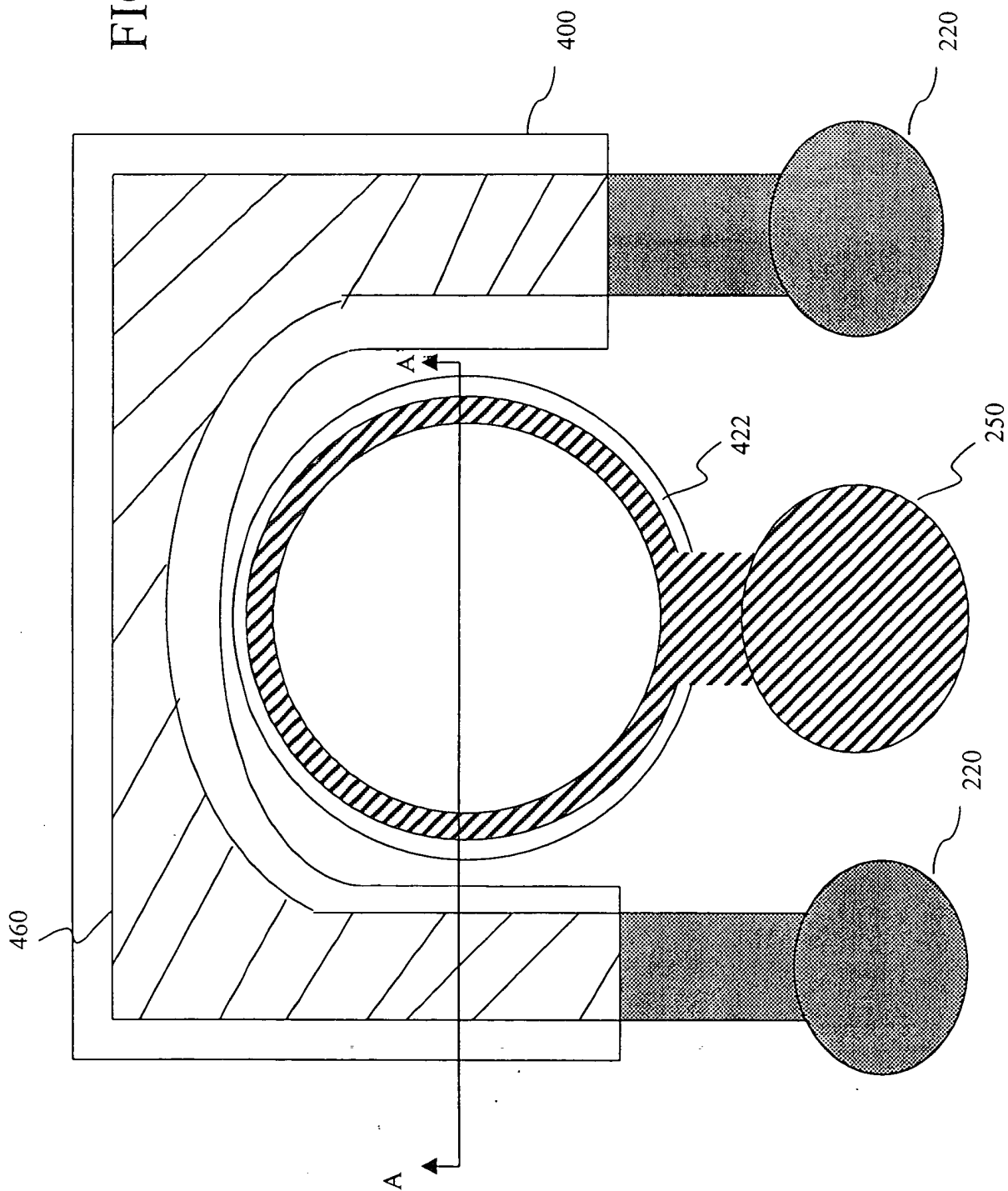
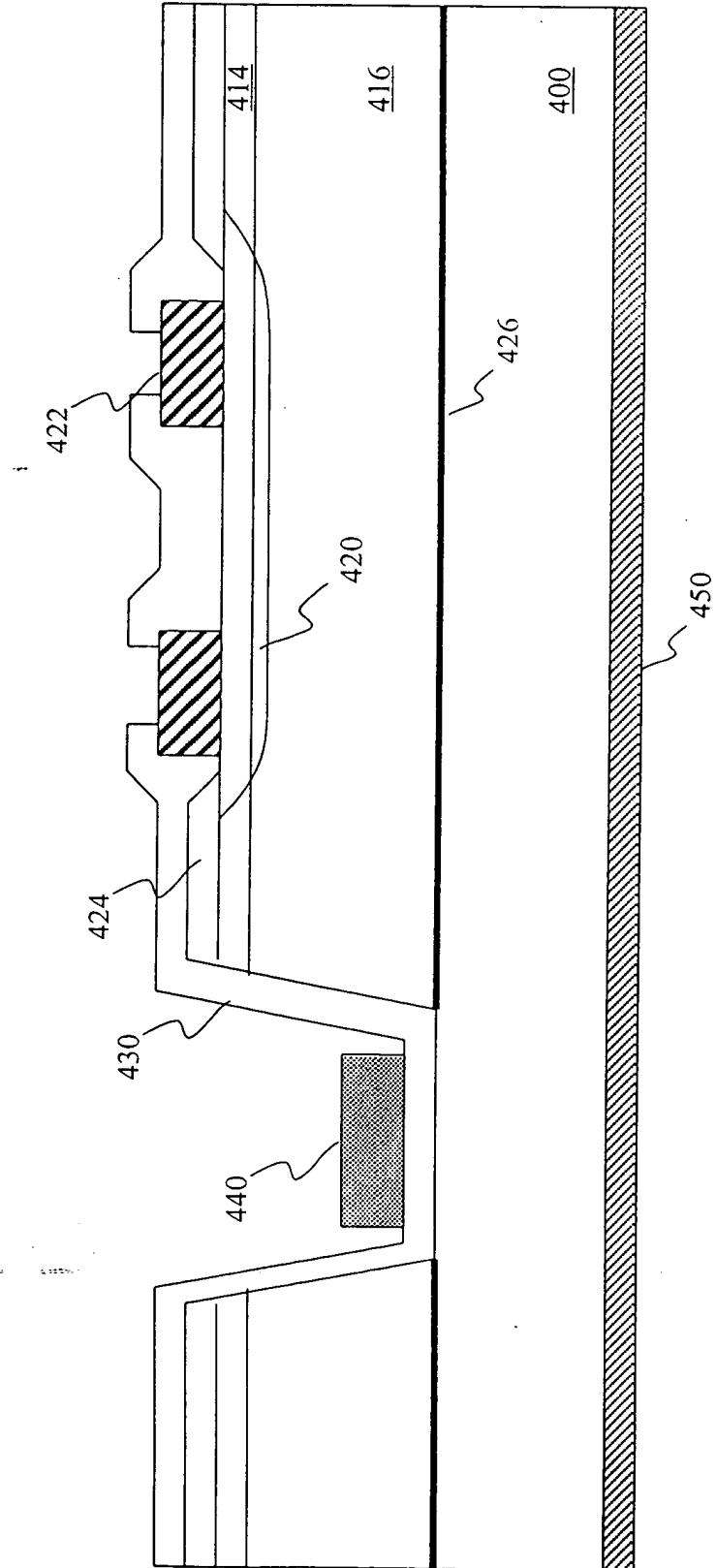
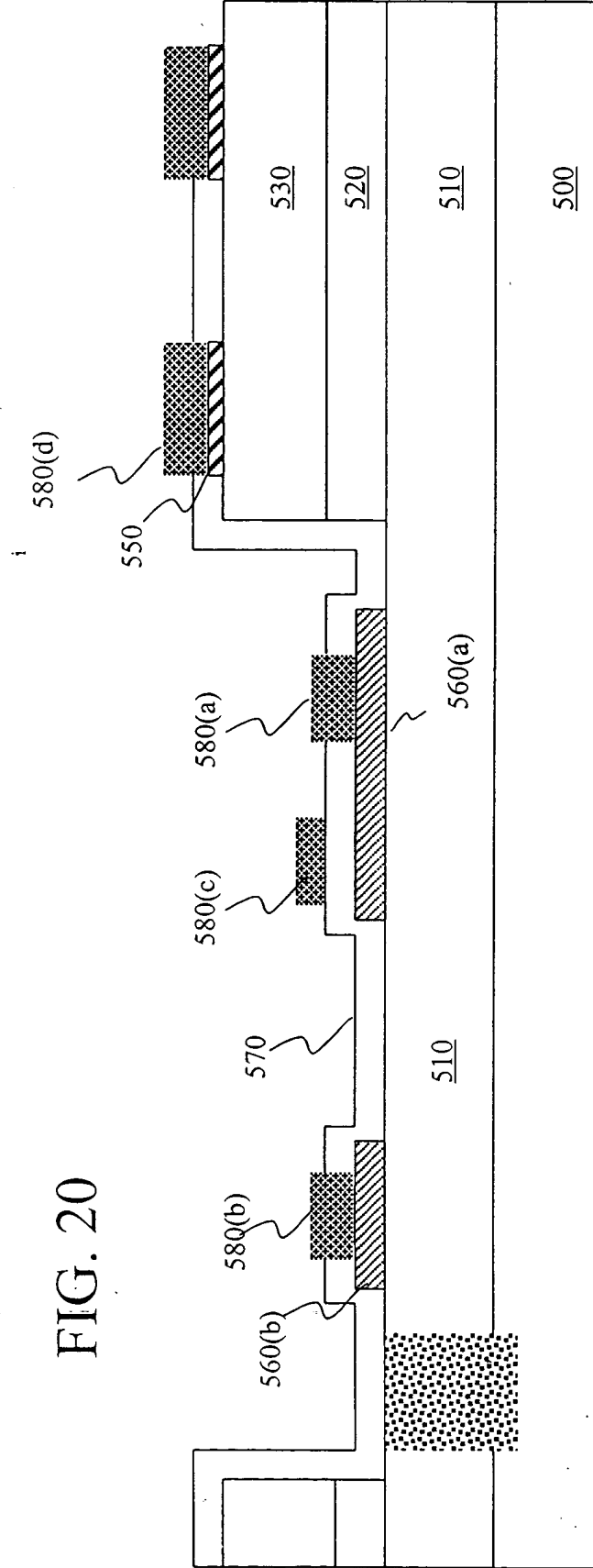


FIG. 19



204740-9844T001

FIG. 20



202720" 9344T001

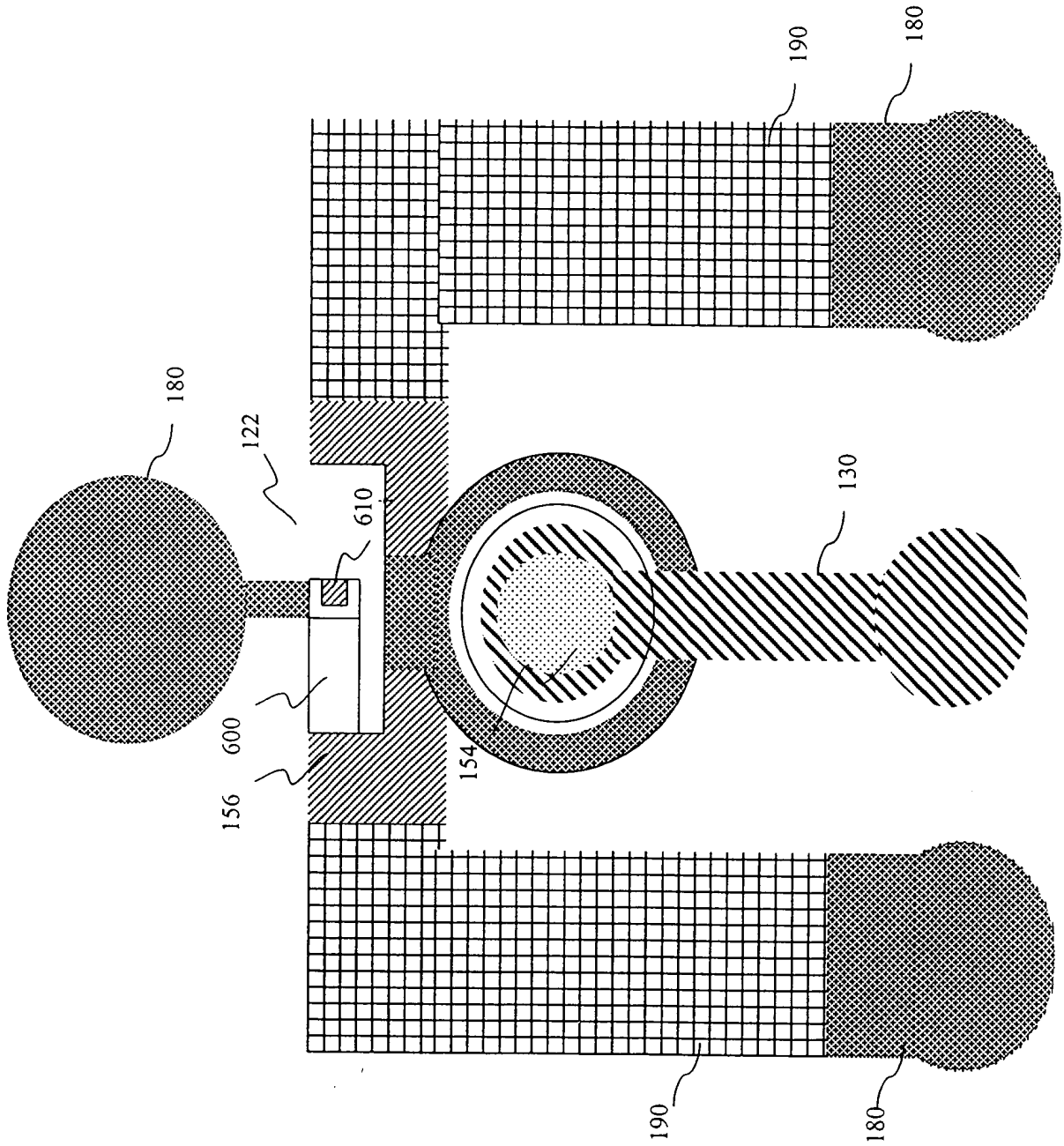
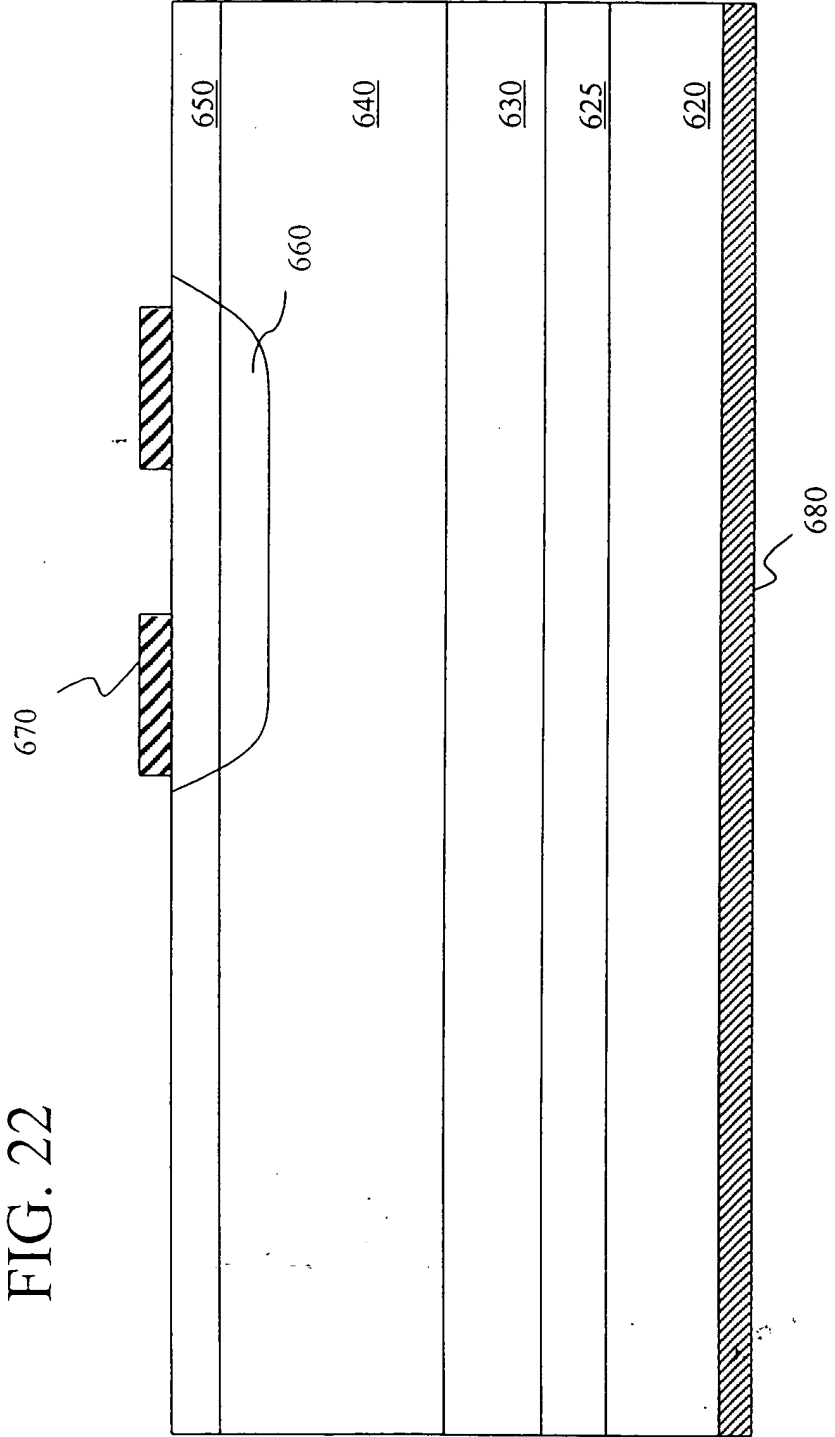


FIG. 21

204740" 9844T00T

FIG. 22



202740 9B44T001

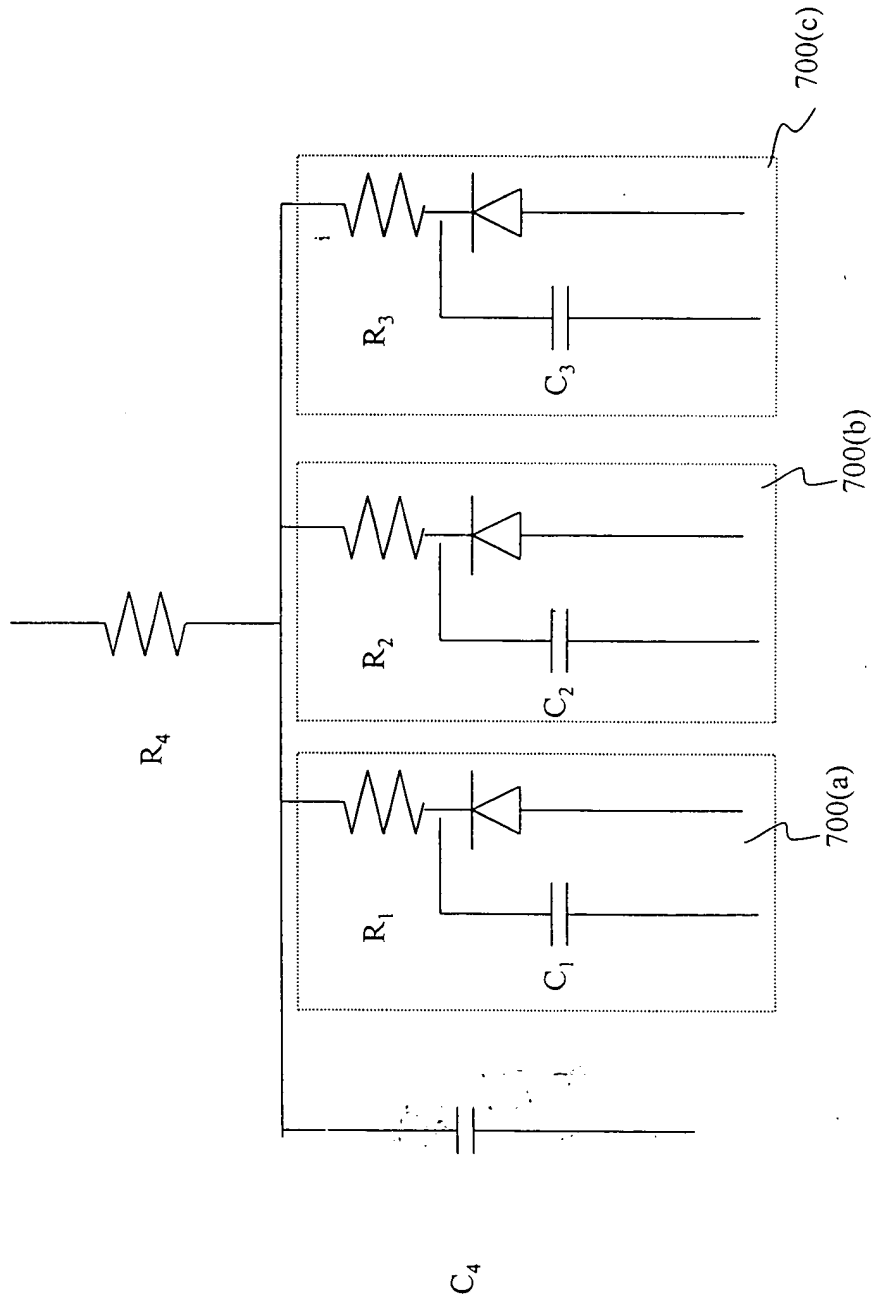


FIG. 23

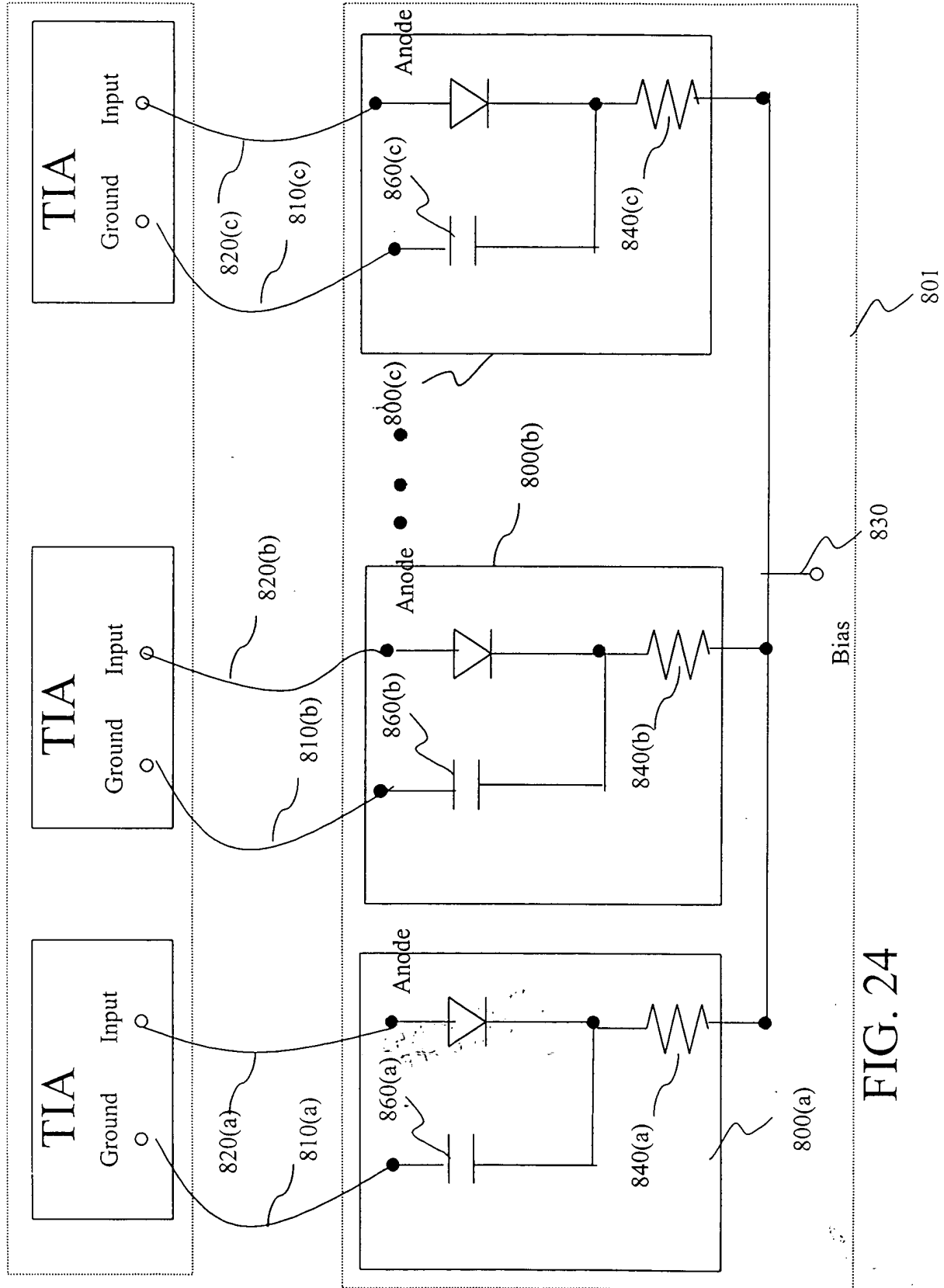


FIG. 24

20240-98441001

FIG. 25

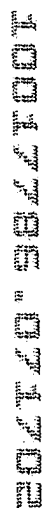


FIG. 26

